



November 2018



- Pletronics' SM55 Series is a quartz crystal controlled precision square wave generator with a CMOS output.
- The package is designed for high density surface mount designs.
- This is a low cost mass produced oscillator.
- Tape and Reel or cut tape packaging is available.
- 0.75 to 50 MHz
- 3.2 x 5 mm LCC Ceramic Package
- Enable/Disable Function
- Disable function includes low standby power mode
- · Low Jitter

Pletronics Inc. certifies this device is in accordance with the RoHS 6/6 (2011/65/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's

Weight of the Device: 0.064 grams

Moisture Sensitivity Level: 1 As defined in J-STD-020C

Second Level Interconnect code: e4

Absolute Maximum Ratings:

Parameter	Unit
V _{CC} Supply Voltage	-0.5V to + 4.0V
Vi Input Voltage	-0.5V to V _{CC} + 0.5V
Vo Output Voltage	-0.5V to V _{CC} + 0.5V
lo Output Current	+20 mA to -20 mA

Thermal Characteristics

The maximum die or junction temperature is 155°C

The thermal resistance junction to board is 30 to 50°C/Watt depending on the solder pads, ground plane and construction of the PCB.



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Part Number:

SM55	45	T	Ε	Z	-50.0M	-XX	
							Packaging code or blank T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel
							Frequency in MHz
							Supply Voltage V _{CC} Z = 0.8 to 2.0 10%
							Optional Enhanced OTR Blank = Temp. range -10 to +70°C C = Temp. range -20 to +70°C E = Temp. range -40 to +85°C
							Series Model
							Frequency Stability 45 = 50 ppm 44 = 25 ppm 20 = 20 ppm
							Series Model

Part Marking and Legend:

P ff.fff M P ff.ff	i II ff.fff M	P5xYWWx • ff.fff M	5xYWWxx ff.fff M • PLExx
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PLE = Pletronics

ff.fff M or ff.ff M = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	6	7	8	9	0	Code	Α	В	С	D	E	F	G	Н	J	K	L	M
Year	2016	2017	2018	2019	2020	Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
(Code		1	2	3	4	5	6	7	8	9	Α	В	С	D	Ε	F	G
	Day		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
(Code		Н	J	K	L	М	N	Р	R	Т	U	٧	W	Χ	Υ	Z	
	Day		17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	



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Electrical Specification for 0.80-2.00V over the specified temperature range

Item	Min	Тур	Max	Unit	Condition
Supply Voltage	0.8		2.0	V	
Frequency Range	0.75		50.0	MHz	
Frequency Accuracy "45"	-50		+50	ppm	For all supply voltages and
"44"	-25		+25		temperatures
"20"	-20		+20		
Output Waveform		СМ	os		
Output High Level	70		-	%	of V _{cc}
Output Low Level	-		30	%	of V _{cc}
Output Symmetry	45		55	%	at 50% point of V _{cc} (See load circuit)
Phase Noise 10Hz		-80		dBc/Hz	
Phase Noise 100Hz		-110		dBc/Hz	
Phase Noise 1kHz		-130		dBc/Hz	
Phase Noise 10kHz		-145		dBc/Hz	
Phase Noise 100kHz		-155		dBc/Hz	
Phase Noise 1MHz		-160		dBc/Hz	
E/D Internal Pull-up	30		150	Kohm	to V _{cc}
V disable	-		30	%	of V _{cc} applied to pin 1
V enable	70		-	%	
Output leakage V _{OUT} = V _{CC}	-		+20	uA	Pin 1 low, device disabled
V _{OUT} = 0V	-20		-	uA	
Standby Current I _{cc}	-		100	uA	
Enable time	-		50	uS	Time for output to reach a logic state
Disable time	-		50	uS	Time for output to reach a high Z state
Start up time	-		5	mS	Time for output to reach specified frequency
Operating Temperature Range	-10		+70	°C	Standard Temperature Range
	-40		+85	°C	Extended Temperature Range "E" Option
Storage Temperature Range	-55		+125	°C	



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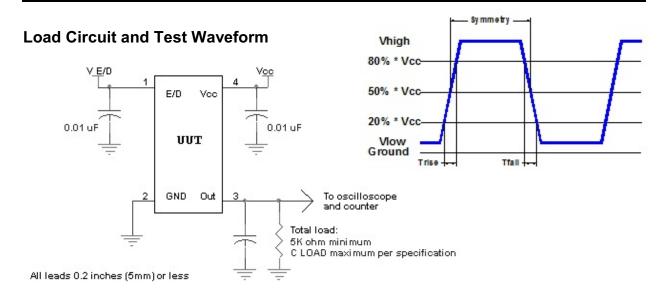
Electrical Specification for 0.80-2.00V over the specified temperature range

Item	Тур	Max	Unit	Condition	
Output T _{RISE} and T _{FALL}	1.3	3.0	nS	Vcc = 1.1 to 2.0V	C _{LOAD} = 15 pF
	1.7	4.0	nS	Vcc = 0.8 to 1.1V	20% to 80% of V _{cc} See Load Circuit
V _{cc} Supply Current (I _{cc})	1.7	2.6	mA	Vcc = 1.5V	
Output frequency: 48.0 MHz	1.3	2.0	mA	Vcc = 1.2V	
	0.9	1.4	mA	Vcc = 0.9V	
V _{cc} Supply Current (I _{cc})	1.1	1.7	mA	Vcc = 1.5V	
Output frequency: 0.75 MHz	0.8	1.2	mA	Vcc = 1.2V	
	0.5	0.8	mA	Vcc = 0.9V	

Specifications with Pad 1 E/D open circuit



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Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions		
Human Body Model	1500	MIL-STD-883 Method 3115		
Charged Device Model	1000	JESD 22-C101		

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

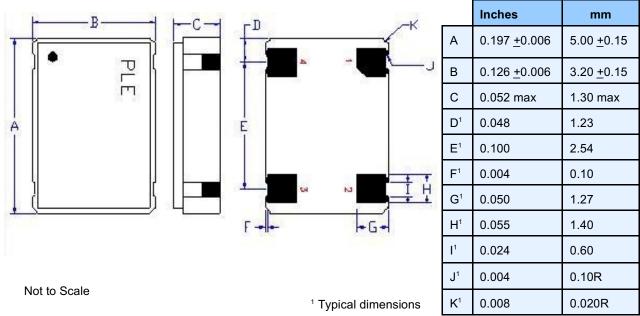
Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant
2nd LvL Interconnect
Category=e4
Max Safe Temp=260C for 10s 2X Max



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Mechanical:



Contacts:

Gold 11.8 to 39.4 µinches (0.3 to 1.0 µm) over Nickel 50 to 350 µinches (1.27 to 8.89 µm)

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is logic low the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm cc}$ if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.

Layout and application information



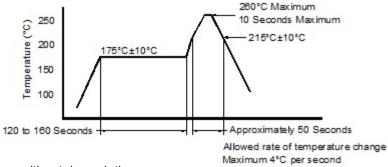
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

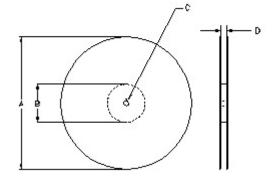
	Constant Dimensions Table 1										
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max			
8mm		1.0			2.0						
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05						
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1			
24mm		1.5			<u>+</u> 0.1						

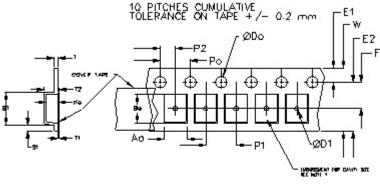
	Variable Dimensions Table 2									
Tape B1 E2 Min F P1 T2 W Ao, Bo & Ko										
16 mm 12.1 14.25 7.5 ±0.1 8.0 ±0.1 8.0 16.3 Note 1										

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm

Not to scale





			REE	L DIMENSI	ONS	
	Α	inches	7.0	10.0	13.0	
		mm	177.8	254.0	330.2	
	В	inches	2.50	4.00	3.75	
		mm	63.5	101.6	95.3	Tape Width
	С	mm	13	3.0 +0.5 / -0	.2	widii
	D	mm	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.4 +2.0 -0.0	16.0
,	Re	el dimen	sions ma	ay vary fro	om the al	oove

USER DIRECTION OF UNREELING -----

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Contacting Pletronics Inc.

Pletronics Inc. Tel: 425-776-1880 19013 36th Ave. West Fax: 425-776-2760

Lynnwood, WA 98036-5761 USA E-mail: ple-sales@pletronics.com

URL: www.pletronics.com

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